Inventor: Jae Jin Lee

Title: Signal Delay Control Circuit in a Semiconductor Device

Sheet 1 of 8

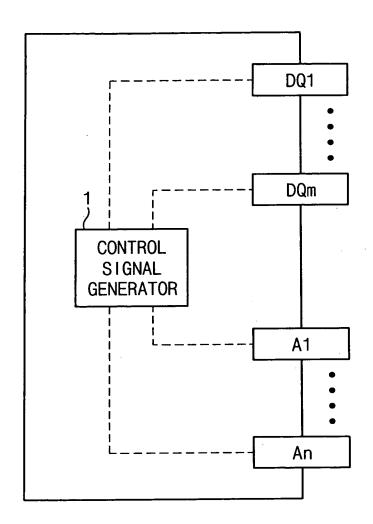


Fig.1

Inventor: Jae Jin Lee

Title: Signal Delay Control Circuit in a Semiconductor Device

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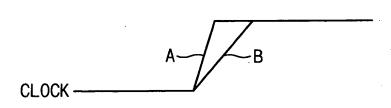
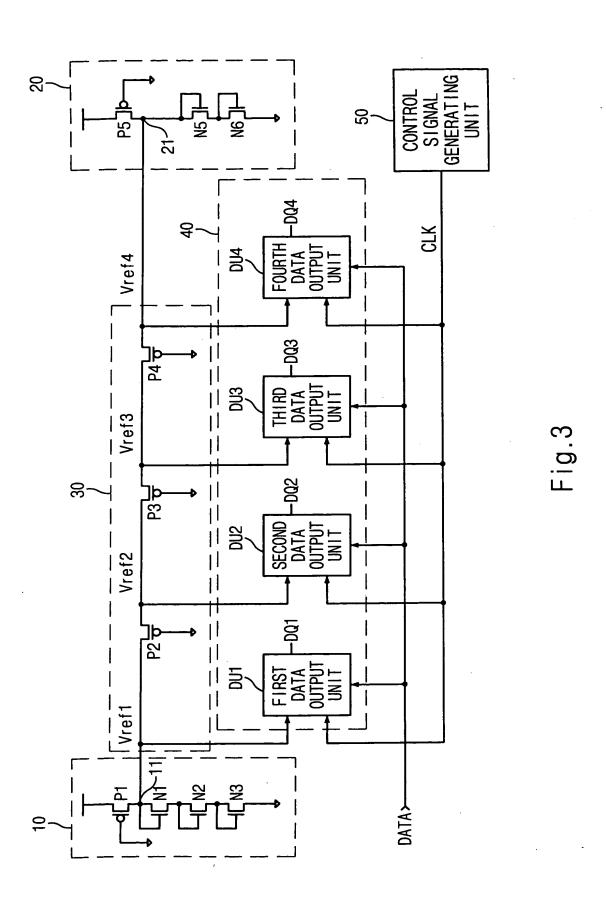


Fig.2

Atty. Docket No. 30205/38071A (Continuation of Serial No. 10/152,267) Inventor: Jae Jin Lee

Title: Signal Delay Control Circuit in a Semiconductor Device

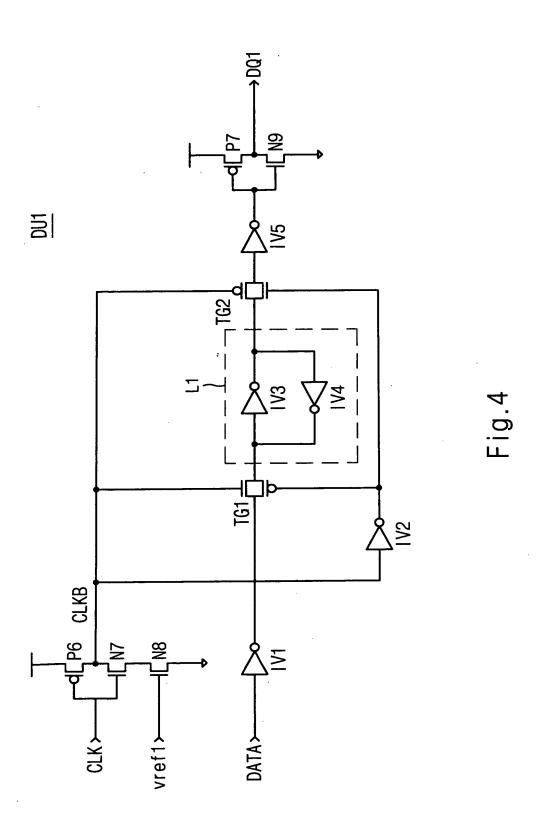
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Inventor: Jae Jin Lee

Title: Signal Delay Control Circuit in a Semiconductor Device

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Inventor: Jae Jin Lee

Title: Signal Delay Control Circuit in a Semiconductor Device

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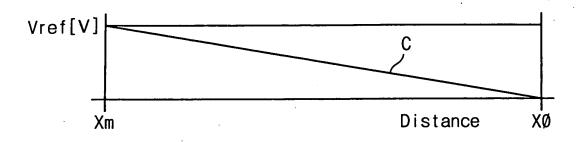


Fig.5

Înventor: Jae Jin Lee

Title: Signal Delay Control Circuit in a Semiconductor Device

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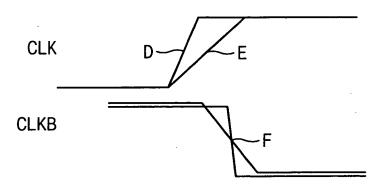


Fig.6

Inventor: Jae Jin Lee

Title: Signal Delay Control Circuit in a Semiconductor Device

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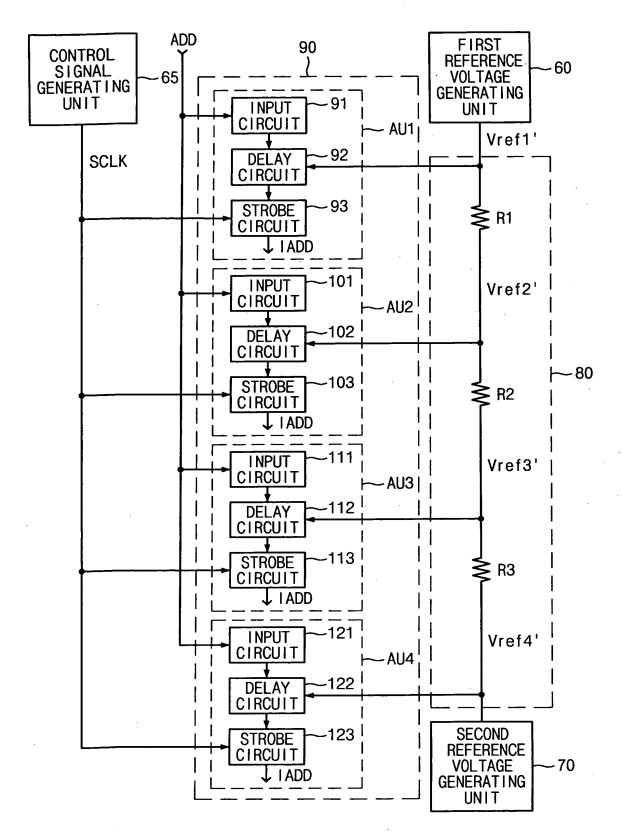
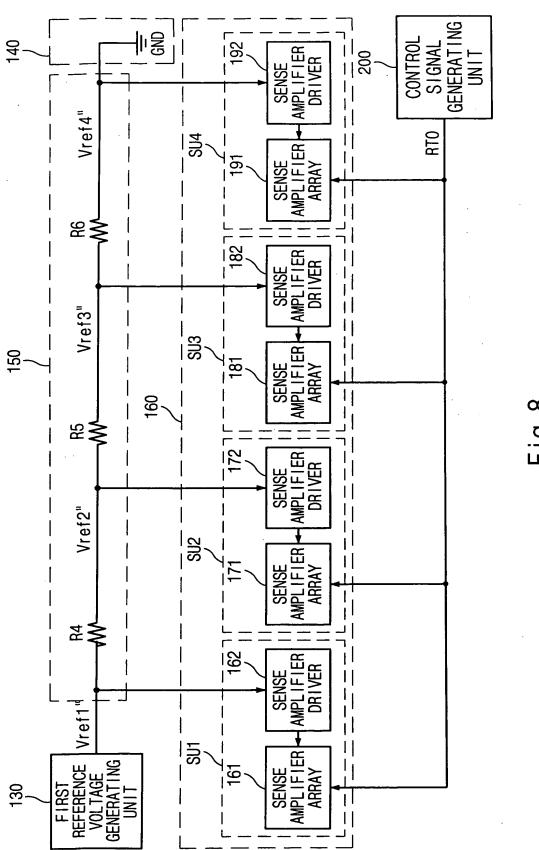


Fig.7

Inventor: Jae Jin Lee

Title: Signal Delay Control Circuit in a Semiconductor Device

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